



FIG.2 PRIOR ART

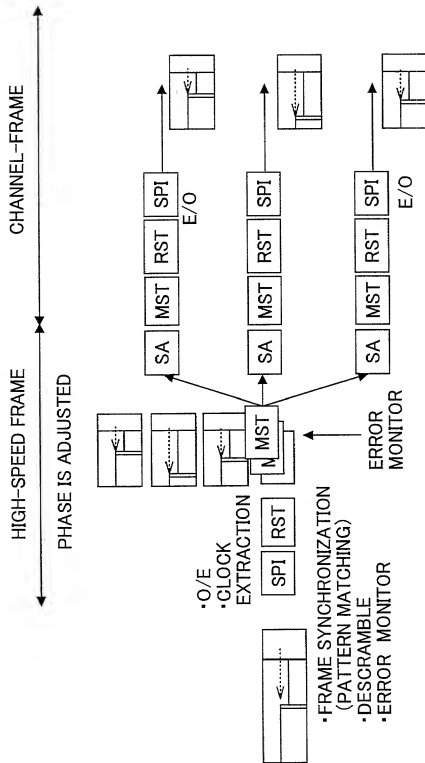


FIG.3A

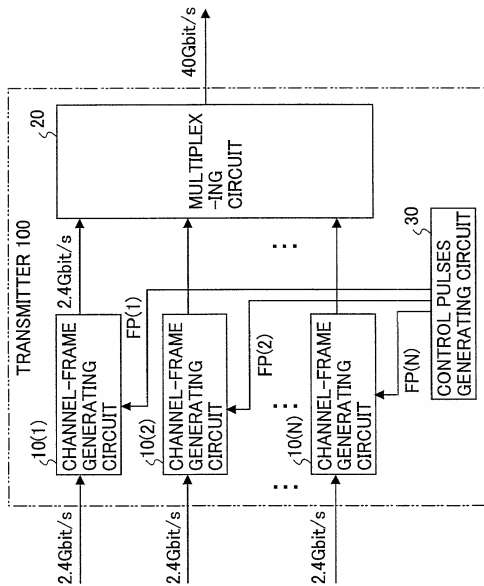


FIG.3B

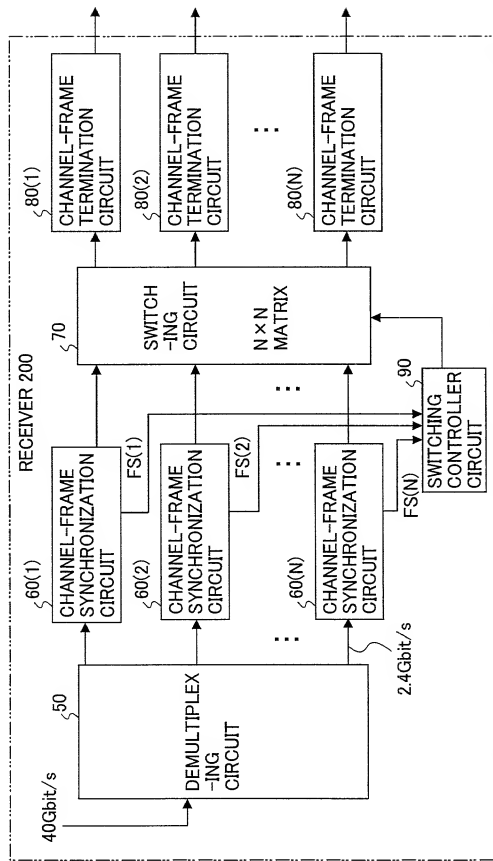


FIG.4

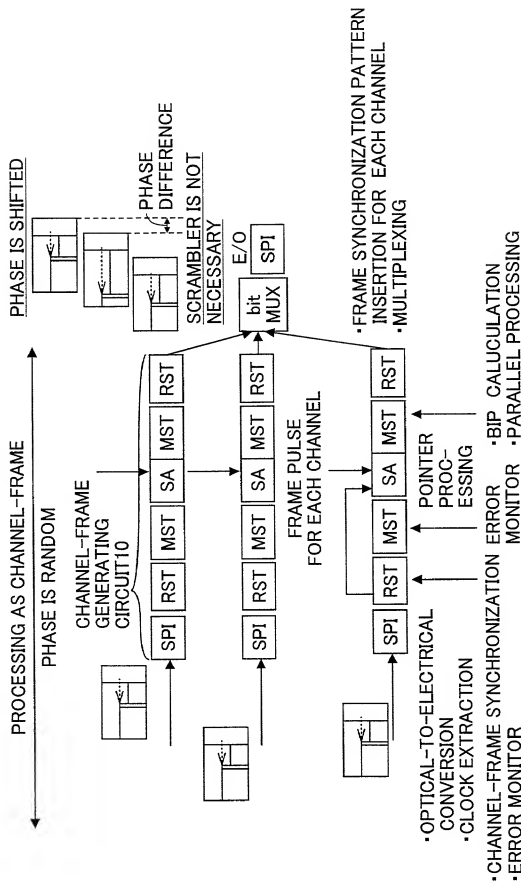




FIG.6A

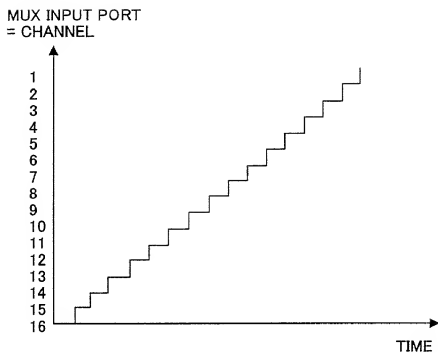


FIG.6B

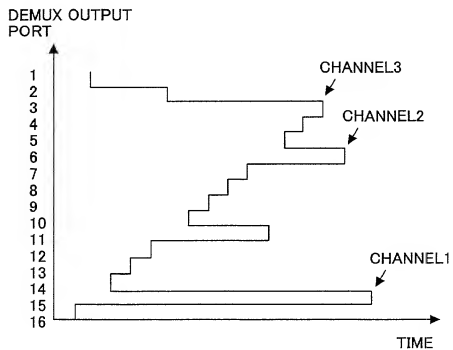
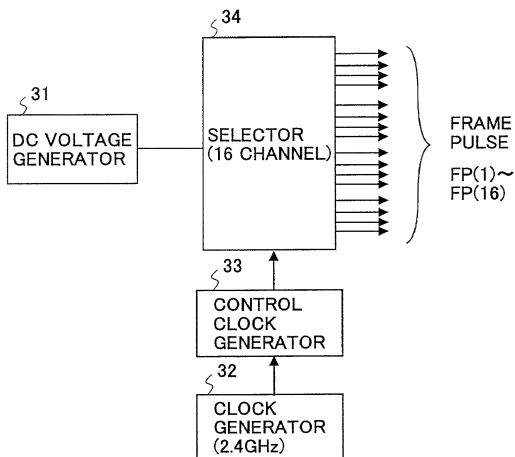


FIG.7



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FIG.8

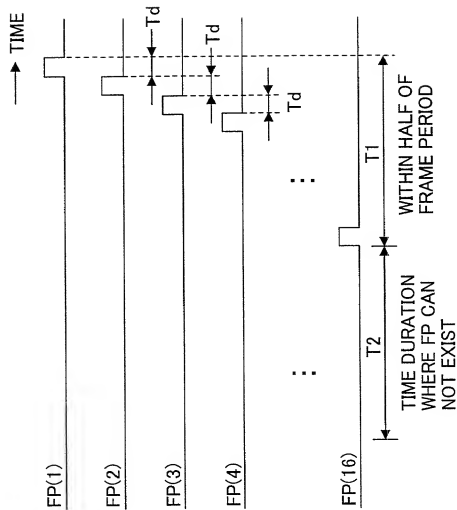


FIG.9

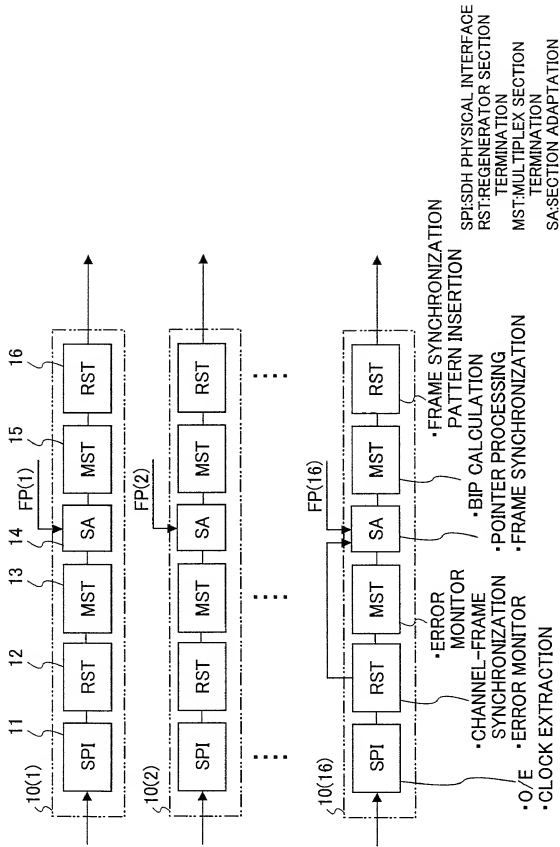


FIG.10

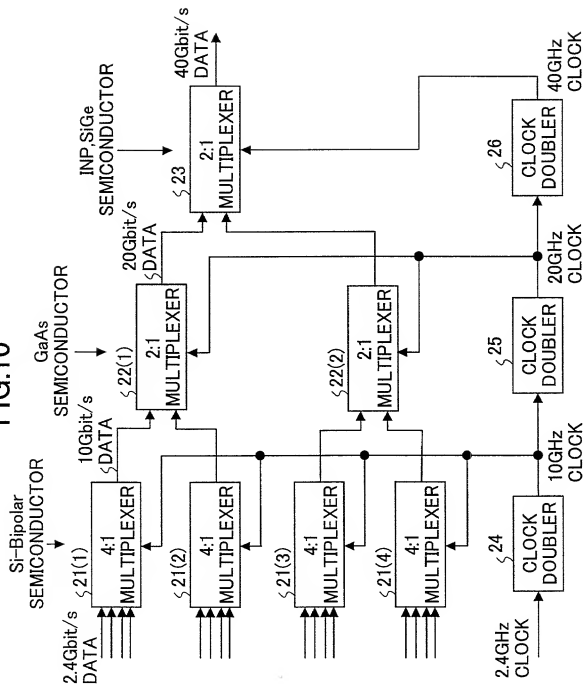


FIG.11

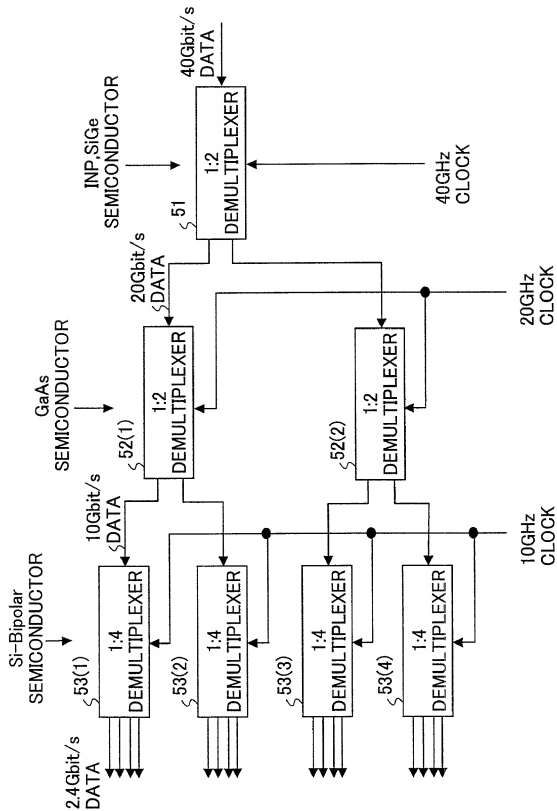
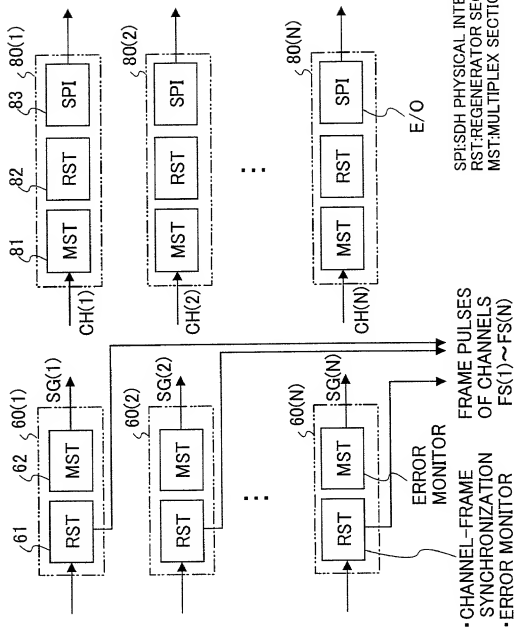


FIG.12



# FIG.13

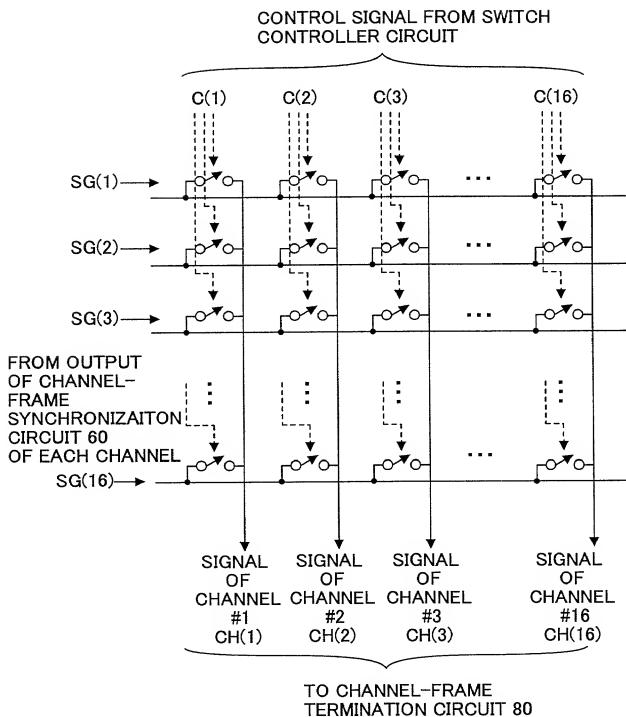


FIG. 14

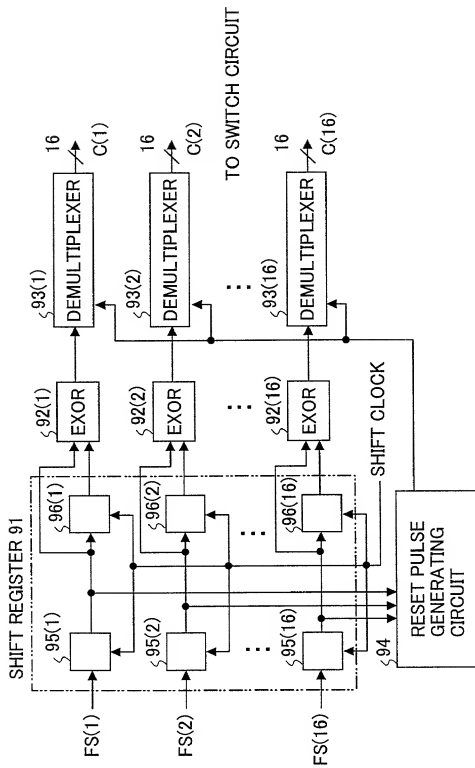


FIG.15A

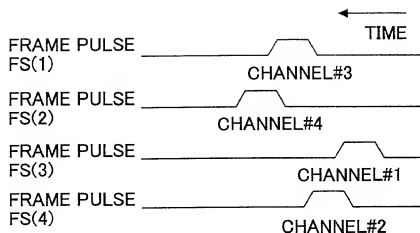


FIG.15B

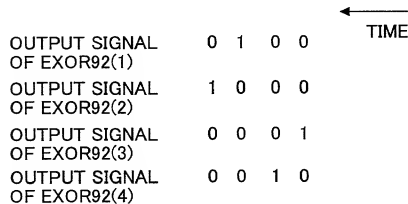


FIG.15C

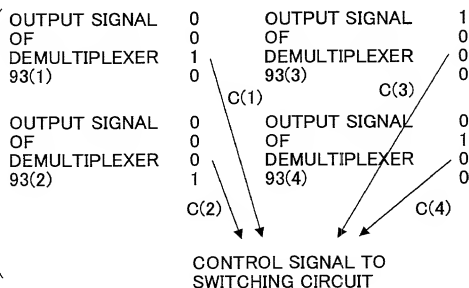
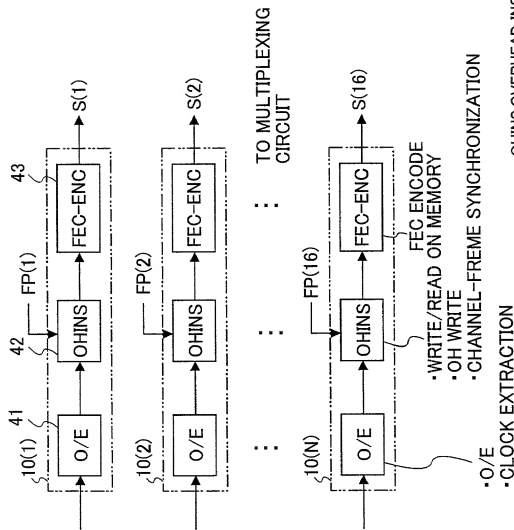




FIG.16



OHINS:OVERHEAD INSERTION CIRCUIT  
FEC-ENC:FORWARD ERROR CORRECTION-ENCODER

FIG.17

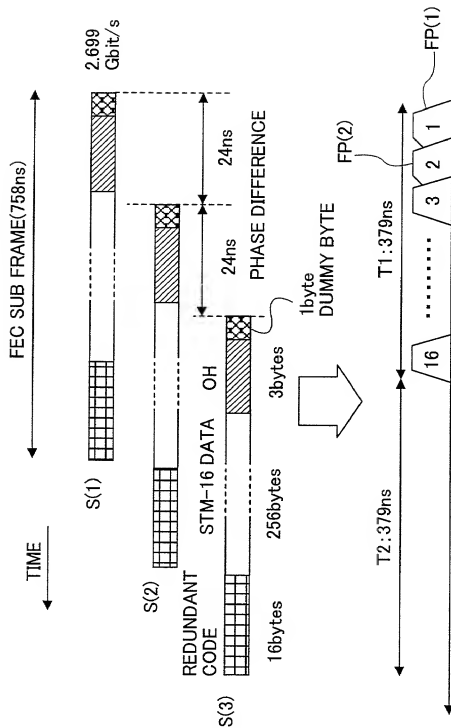


FIG.18

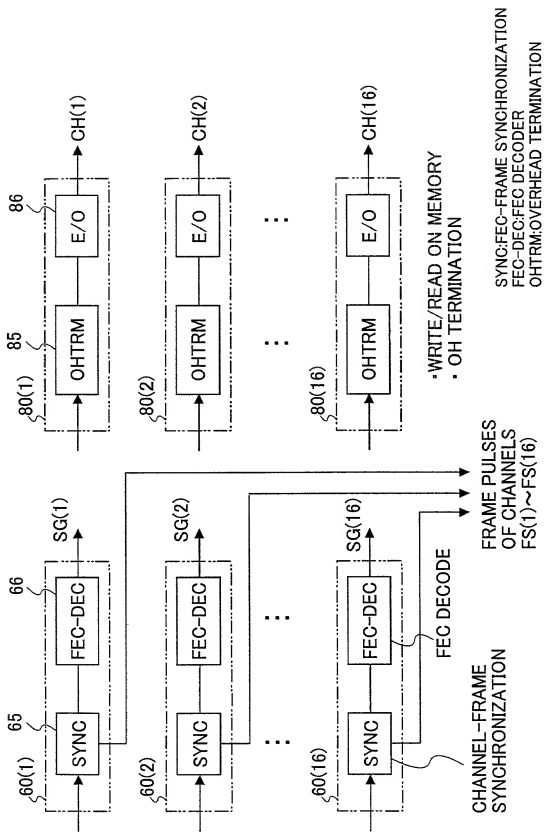


FIG.19

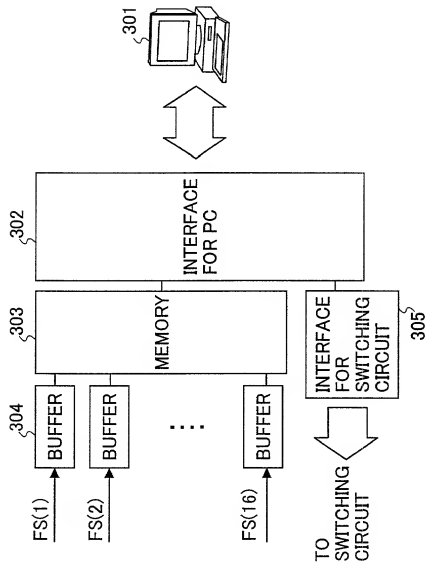
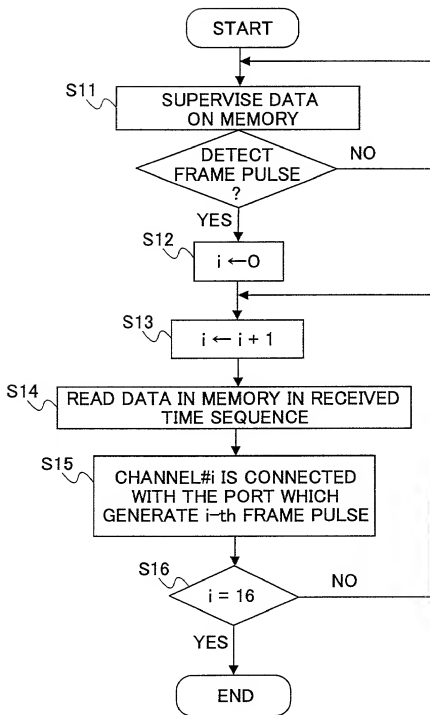


FIG.20



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